

Application Serial No. 09/898,295

**Amendments To Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1-12. (Cancelled)

13. (Currently Amended) A method of synchronizing ~~the~~ a digital transmission of analog modem signals, comprising:

receiving, at a second modem, a first digital data signal from a first modem via a transmission medium, wherein:

said first digital data signal is received serially, and

said first digital data signal comprises a first sample clock signal, said first sample clock signal comprising a first start bit representing a delay;

generating, by said second modem, a first recreation clock signal based on said first start bit, wherein said first recreation clock signal comprises said delay;

detecting said first start bit by said second modem;

converting said first digital data signal to a first analog signal by a first digital-to-analog converter;

generating, by said second modem, a second analog signal, ~~wherein~~ said second analog signal comprises a second sample clock signal, said second sample clock signal comprising a second start bit and a second sample clock signal;

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converting, by a first analog-to-digital converter, said second analog signal to a second digital data signal; and

transmitting, by said second modem, said second digital data signal to said first modem via said transmission medium.

14. (Original) A method of synchronizing the digital transmission of analog signals, as recited in claim 13, wherein said transmission medium is an A/C power line.

15. (Original) A method of synchronizing the digital transmission of analog signals, as recited in claim 13, wherein said transmission medium is an over the air RF signal.

16. (Currently Amended) A method of synchronizing the a digital transmission of analog signals, as recited in claim 13, further comprising:

receiving, at said first modem, said second digital data signal from said second modem;

detecting said second start bit by said first modem;

generating, by said first modem, a second recreation clock signal based on said second start bit, wherein said second recreation clock signal comprises said delay;

converting said second digital data signal to a second analog signal by a second digital-to-analog converter;

generating, by said first modem, a third analog signal, ~~wherein~~ said third analog signal comprises a first sample clock signal, said first sample clock signal comprising a third start bit ~~and said first sample clock signal;~~

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converting, by a second analog-to-digital converter, said third analog signal to a third digital data signal; and

transmitting, by said first modem, said third digital data signal to said first modem via said transmission medium.